

**Version „E“      #342002      Seite 2**

**Version „cr“      #251027      Seite 3–5**

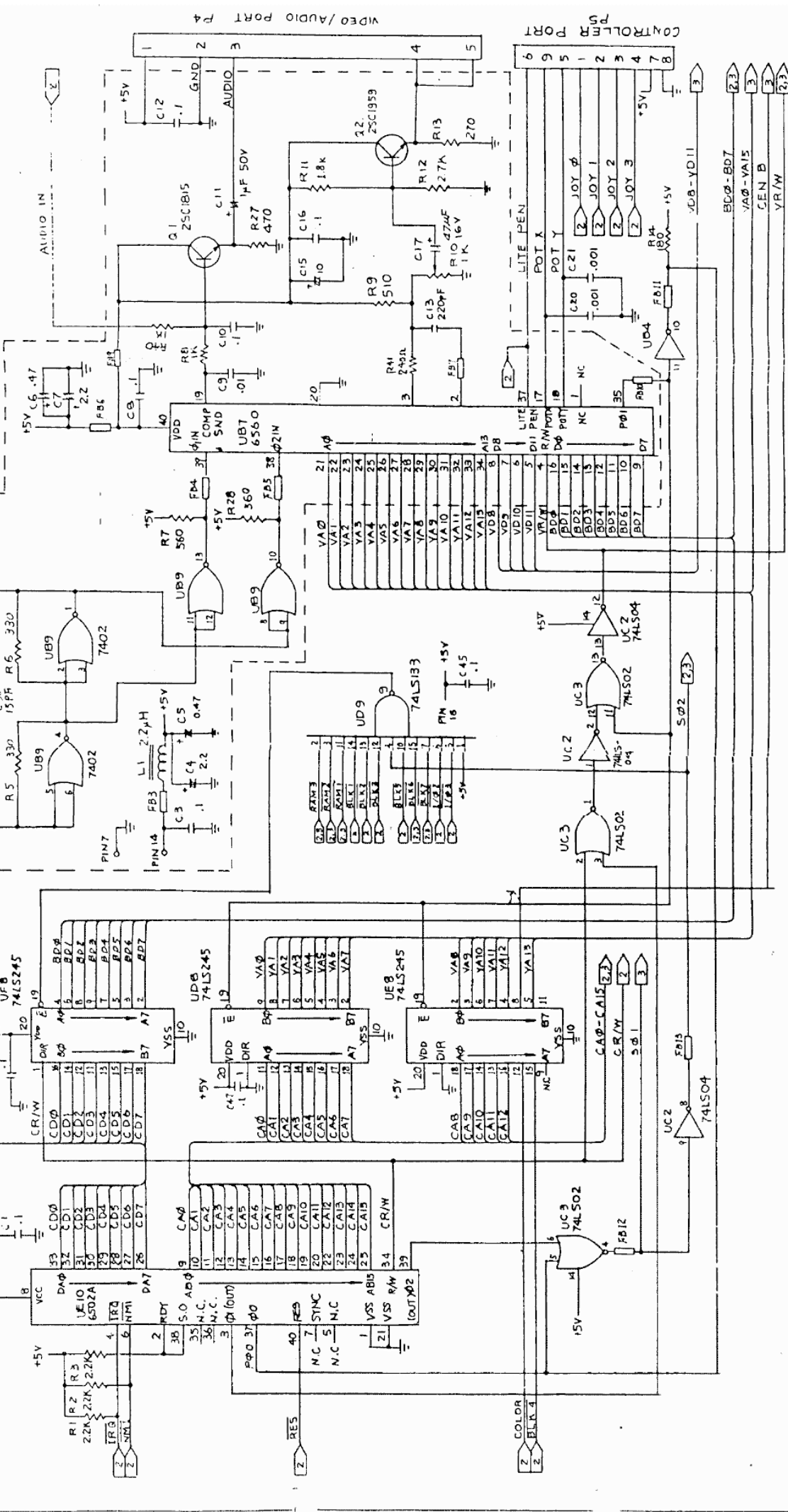
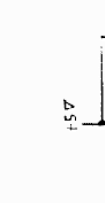
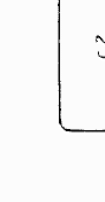
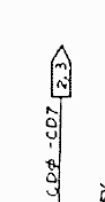
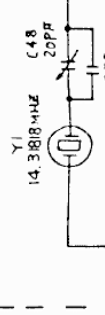
# SCHEMATIC MAIN LOGIC VERSION E



DATE APPROVAL  
11/11/73  
9/11/73

LTR	ZONE	DESCRIPTION
B		REVISED PER ECO 82000-1
C		REVISED PER ECO 82000-2
D		REVISED PER ECO 82000-3

Y1  
14.318MHz  
C48  
20PF



UNLESS OTHERWISE SPECIFIED		DATE	DRIVEN BY	SCALE	MONITOR	SHEET	OF
TOLERANCES ON		11/11/73	7402	1:1	1	1	1
DIMENSIONS		11/11/73	7404	1:1	1	1	1
DECIMALS		11/11/73	7412	1:1	1	1	1
FRACTIONS		11/11/73	7413	1:1	1	1	1
MATERIAL		11/11/73	7415	1:1	1	1	1
FINISH		11/11/73	7416	1:1	1	1	1

COMMODORE		DATE	DRIVEN BY	SCALE	MONITOR	SHEET	OF
SCHEMATIC DIAGRAM		11/11/73	7402	1:1	1	1	1
VIC 20CR		11/11/73	7404	1:1	1	1	1
SIZE		11/11/73	7412	1:1	1	1	1
C 251027 01 D		11/11/73	7413	1:1	1	1	1
REVISION		11/11/73	7415	1:1	1	1	1
NTSC		11/11/73	7416	1:1	1	1	1



# REVISIONS

DESCRIPTION

SEE SHEET 1

LTR ZONE

DATE

APPROV

DATE

APPROV

DATE

APPROV

DATE

APPROV

DATE

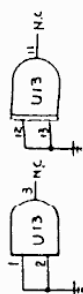
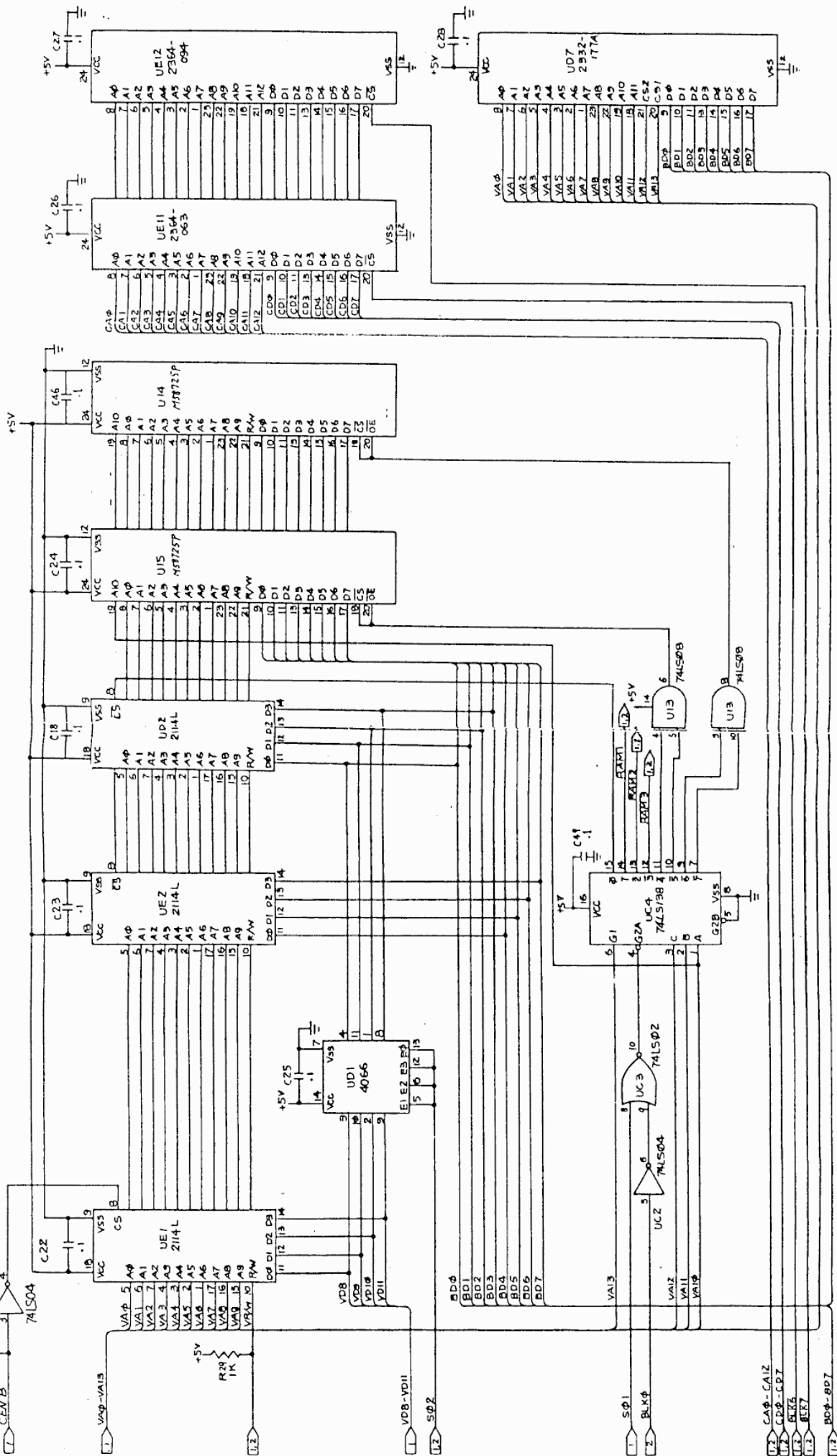
APPROV

DATE

APPROV

DATE

APPROV



commodore

SCHEMATIC DIAGRAM

VIC 20 CPU

SIZE C 251027-01 REV D

SCALE NONE SHEET 3 OF 3

DATE	07/11/82
DESIGNED BY	W. J. V.
CHECKED BY	W. J. V.
APPROVED BY	W. J. V.
USED ON	VIC 20 CPU
MATERIAL	NTSC
FORM	