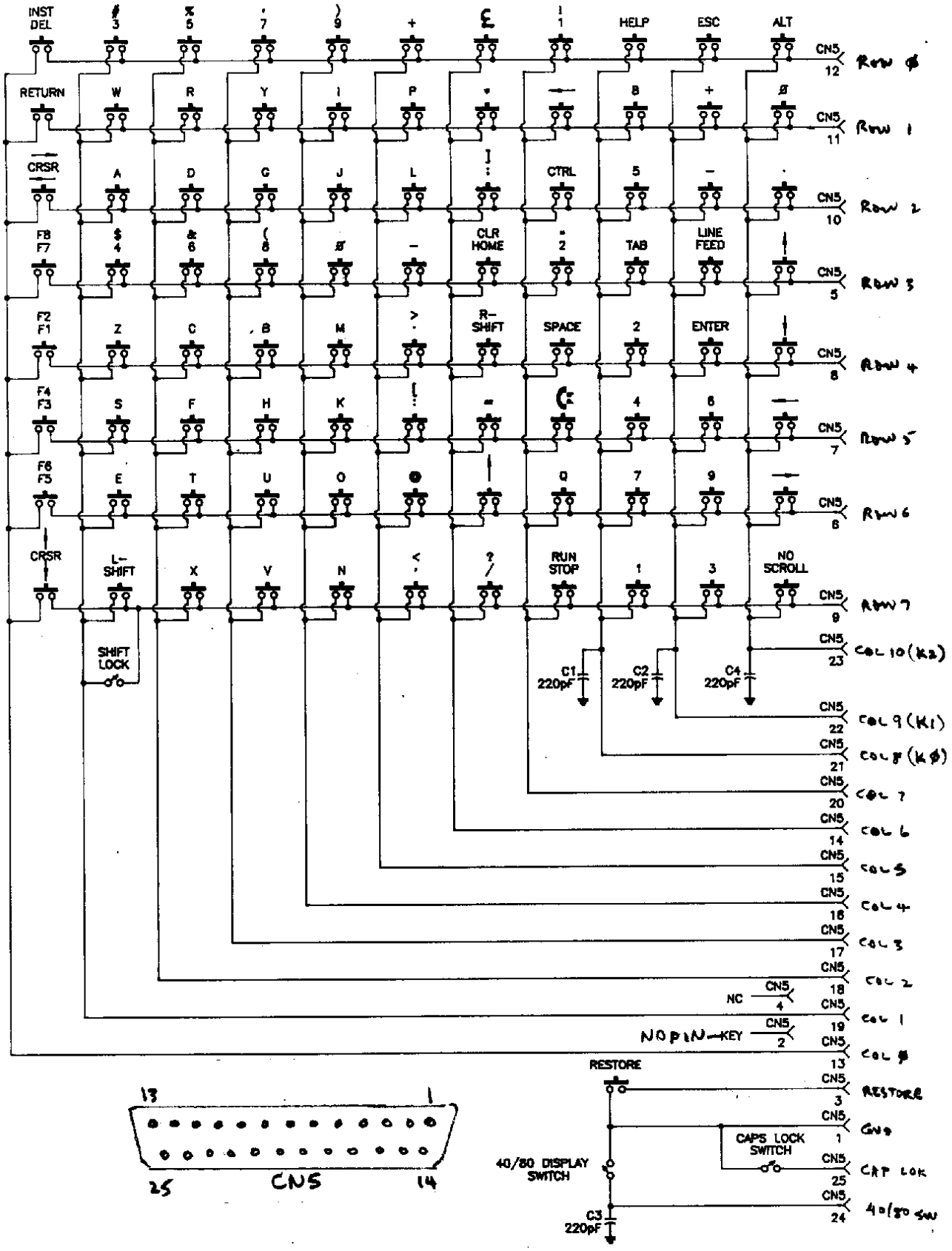
CBM C 128 D „cr"

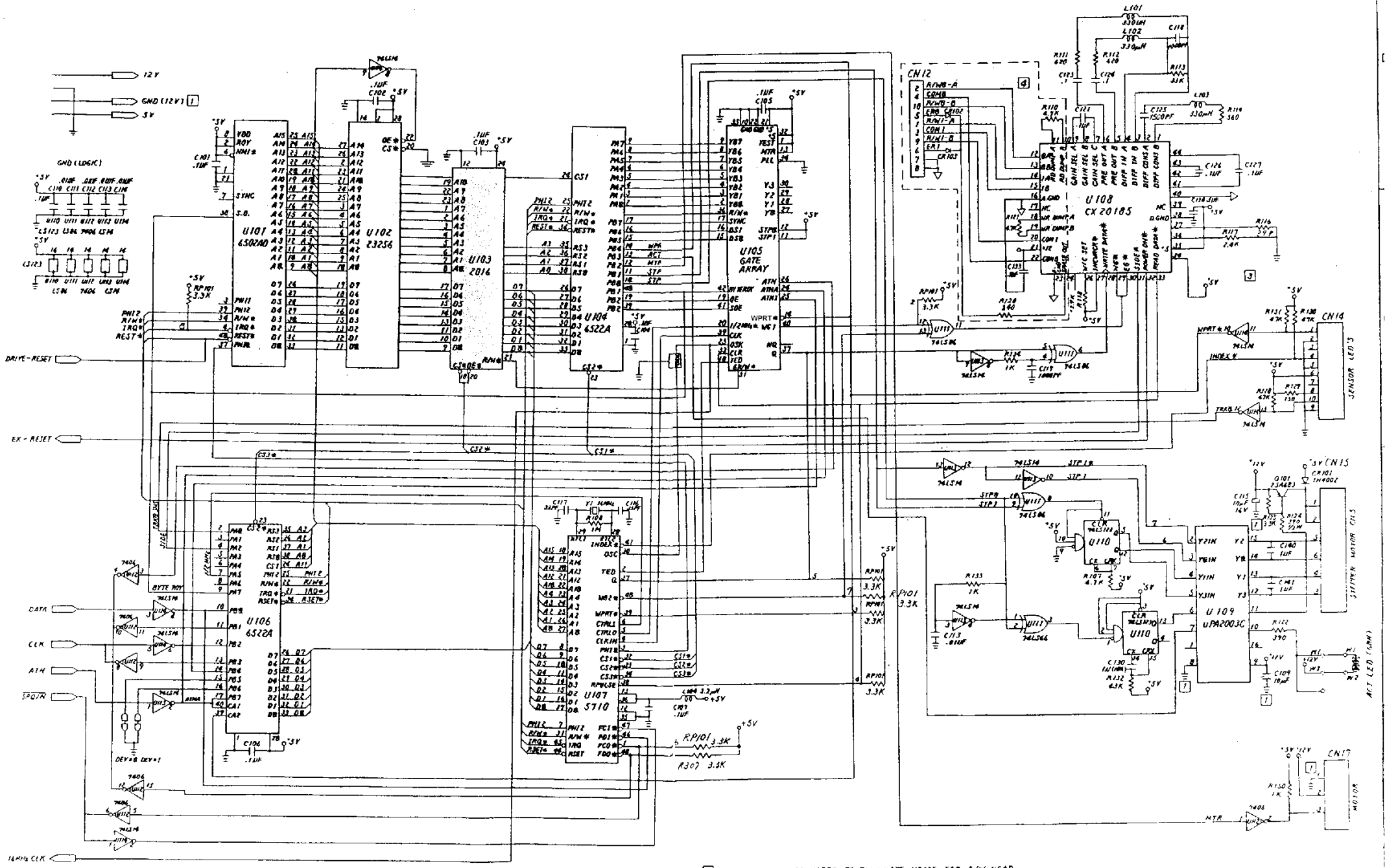
From FUNET.FI:
These are the schematic diagrams of the Commodore 128DCR main board (metal case C128D with built-in 1571CR floppy controller). The diagrams were scanned from a 64'er Sonderheft, and they appear to be a copy of schematic 252451.

According to Nicolas Welte, there is an error on the right half of page 5. The 74LS14 hex Schmitt-trigger inverter U113, pin 13 is connected to WPRT* (misprinted WTRT* on U105), which doesn't make any sense. Obviously it should be connected to STP1 instead, which is three lines more to the right!

NOTE: This FDD.GIF file is a corrected page. Ray Carlsen 7-04

8 ROWS x 11 COLUMNS MATRIX



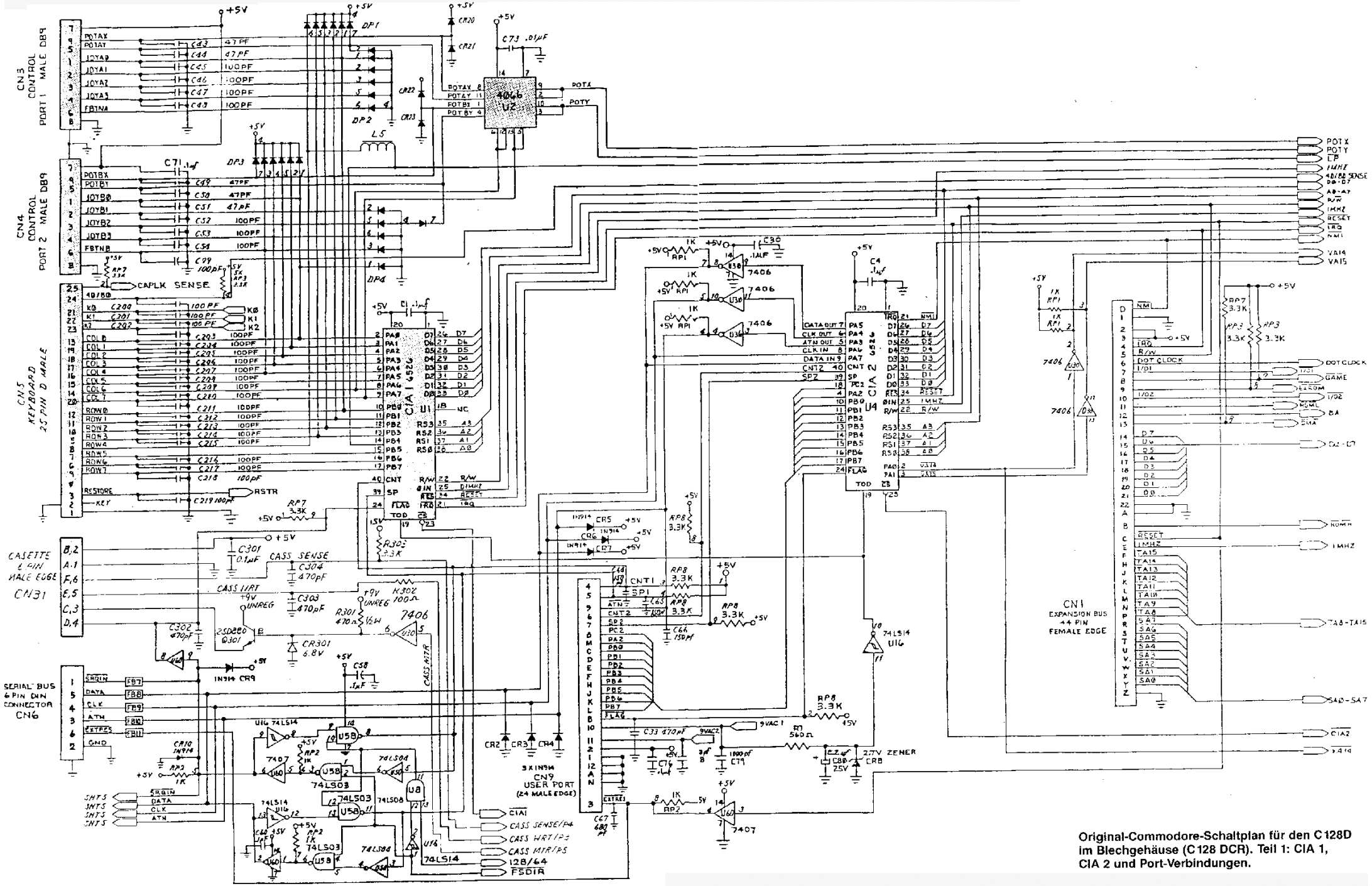


FDD CONTROL

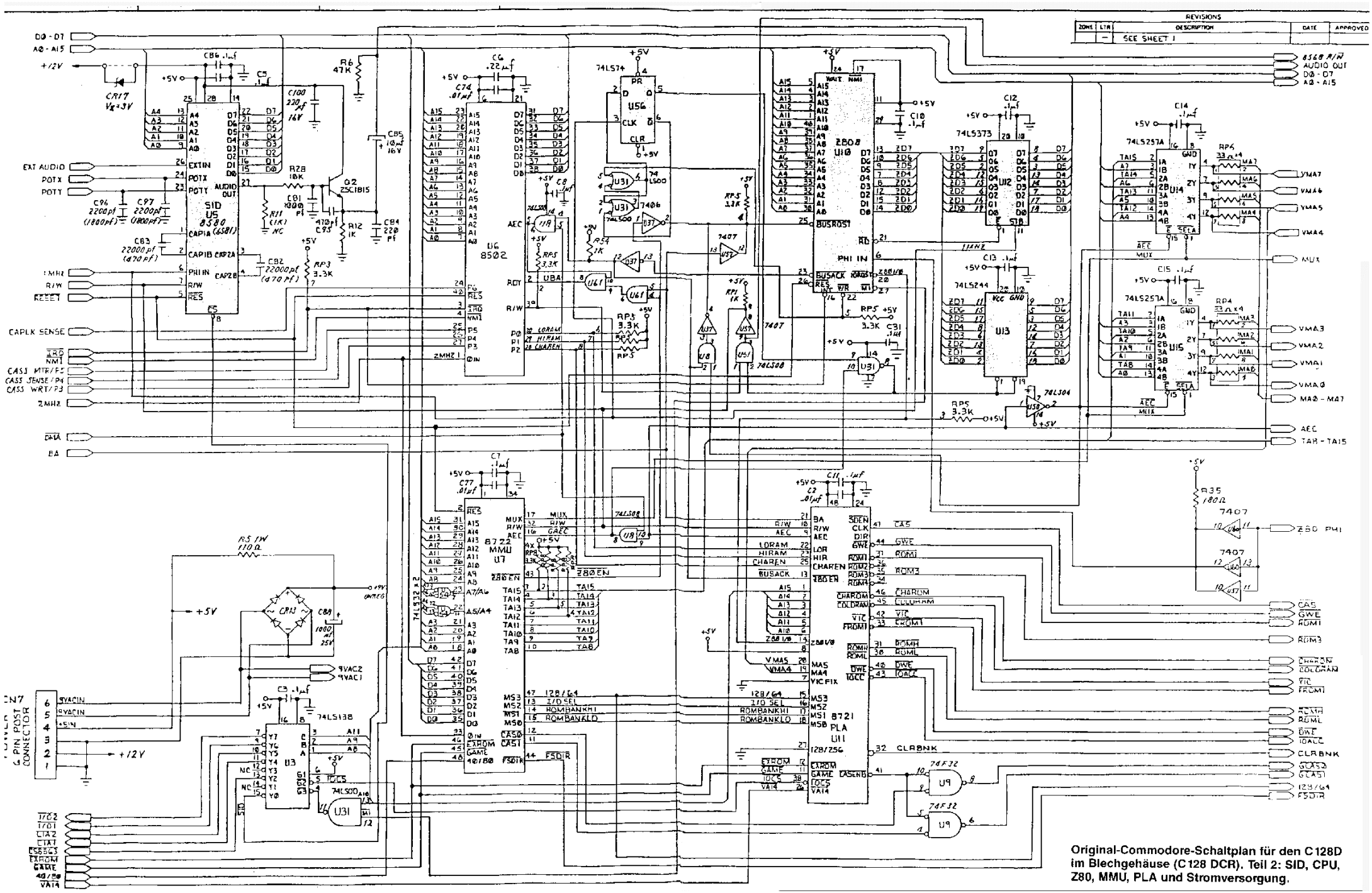
[4] GND GUARD IS REQUIRED TO ELIMINATE NOISE FOR R/W HEAD.
[2] X=F & XX VALUE WILL MEET FUJITSU R/W IC / OPTION.
[3] THIS SCHEMATIC MEETS NEWTRONICS D502 DISK DRIVE.
[1] STEPPER GND : LOW IMPEADANCE GND CONNECTION IS REQUIRED BETWEEN EACH POINT TO ELIMINATE NOISE.

NOTES-UNLESS OTHERWISE SPECIFIED:

Original-Commodore-Schaltplan für den C128D im Blechgehäuse (C128 DCR). Teil 5: Steuerung für das Disketten-Laufwerk.

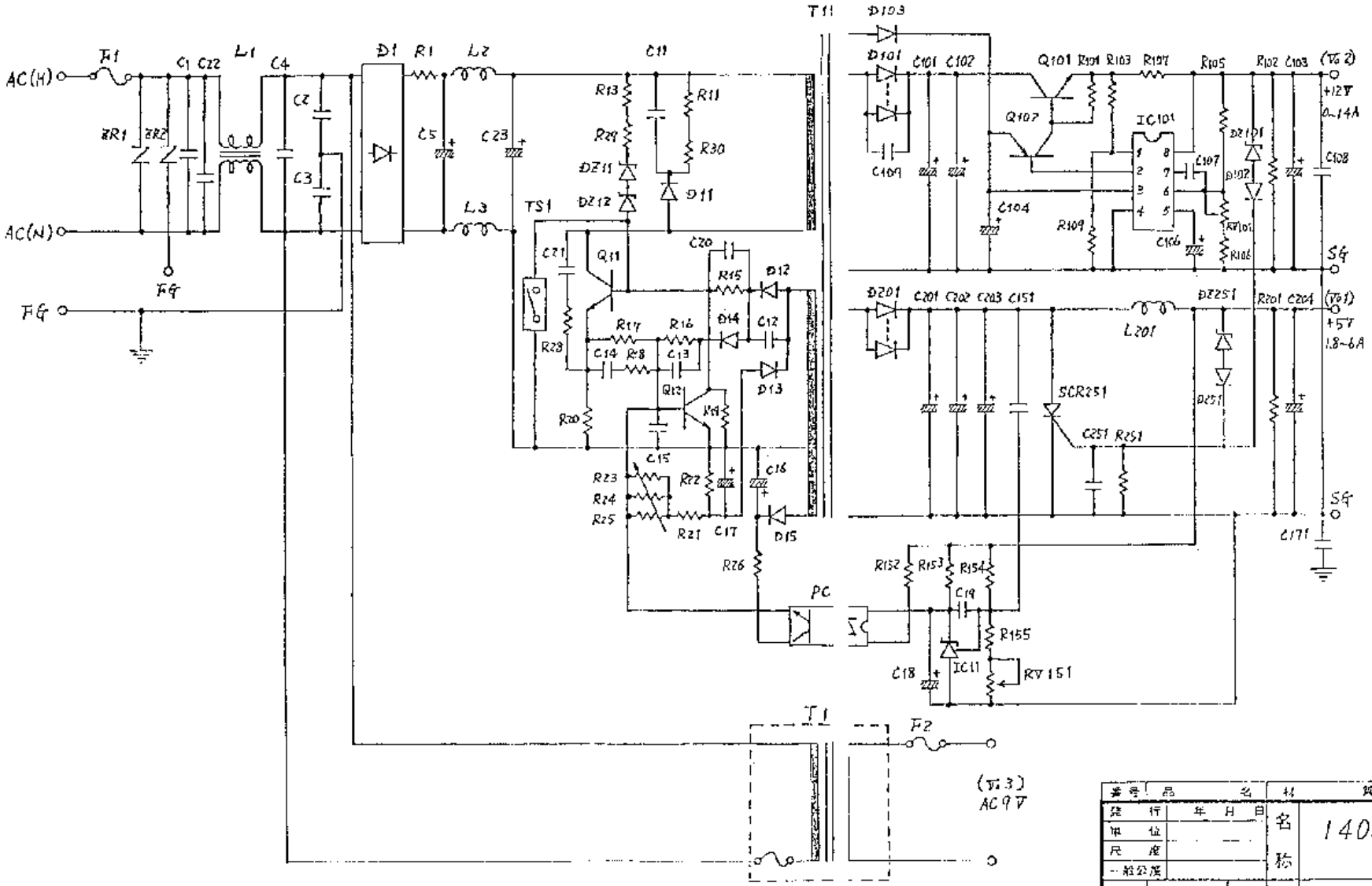


Original-Commodore-Schaltplan für den C128D im Blechgehäuse (C128 DCR). Teil 1: CIA 1, CIA 2 und Port-Verbindungen.

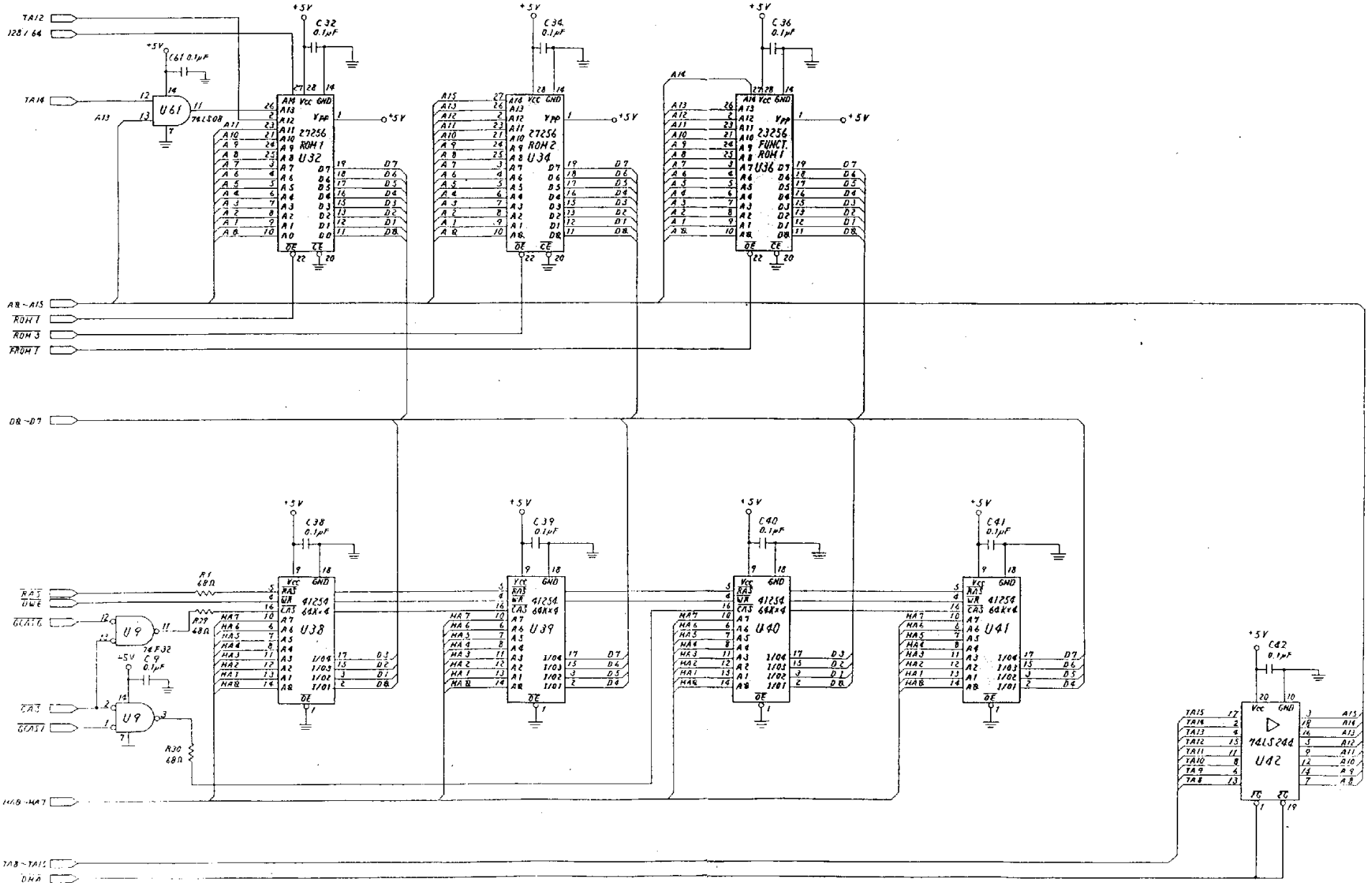


Original-Commodore-Schaltplan für den C128D im Blechgehäuse (C128 DCR). Teil 2: SID, CPU, Z80, MMU, PLA und Stromversorgung.

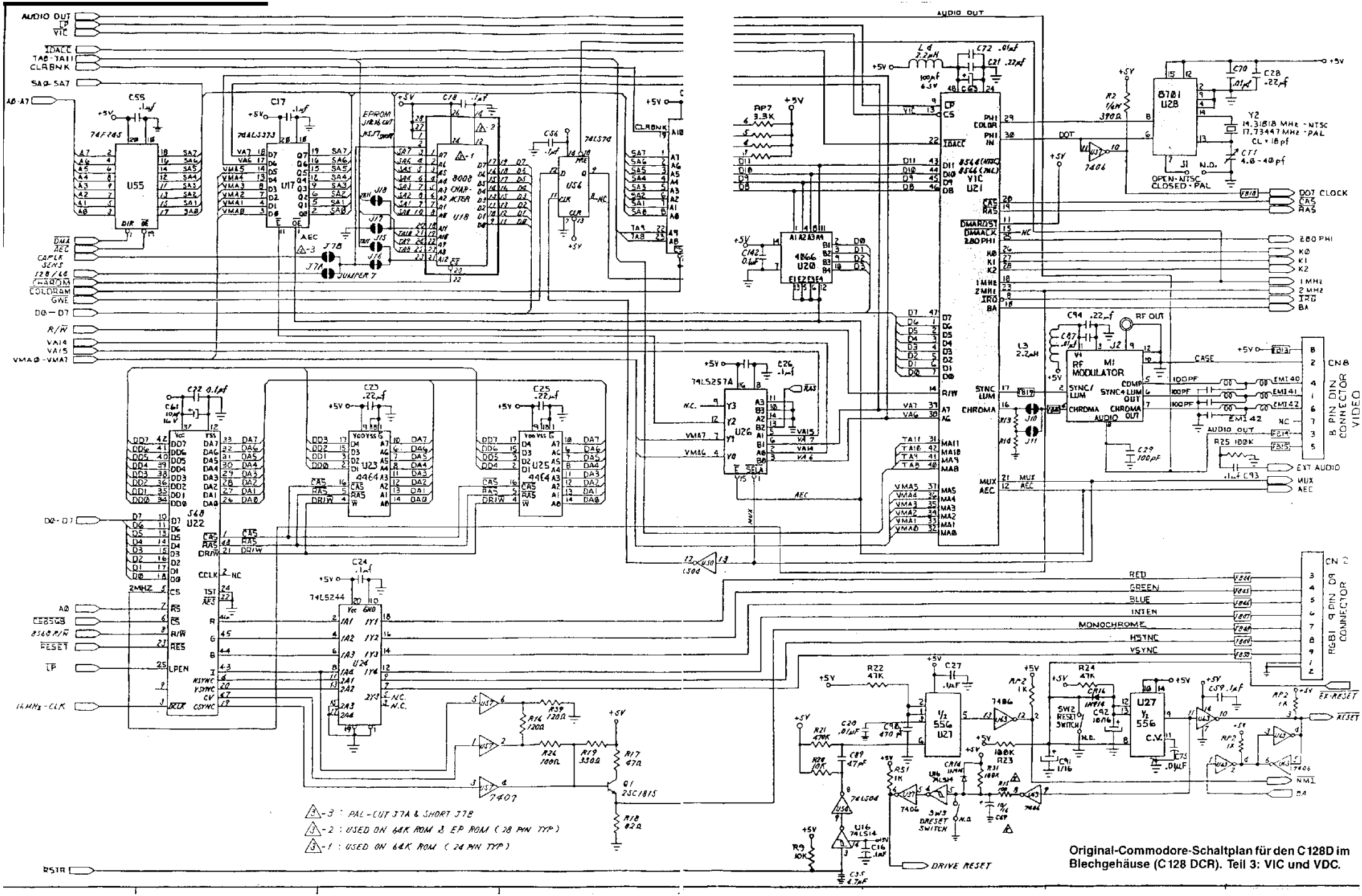
TOHOKU METAL INDUSTRIES, LTD.



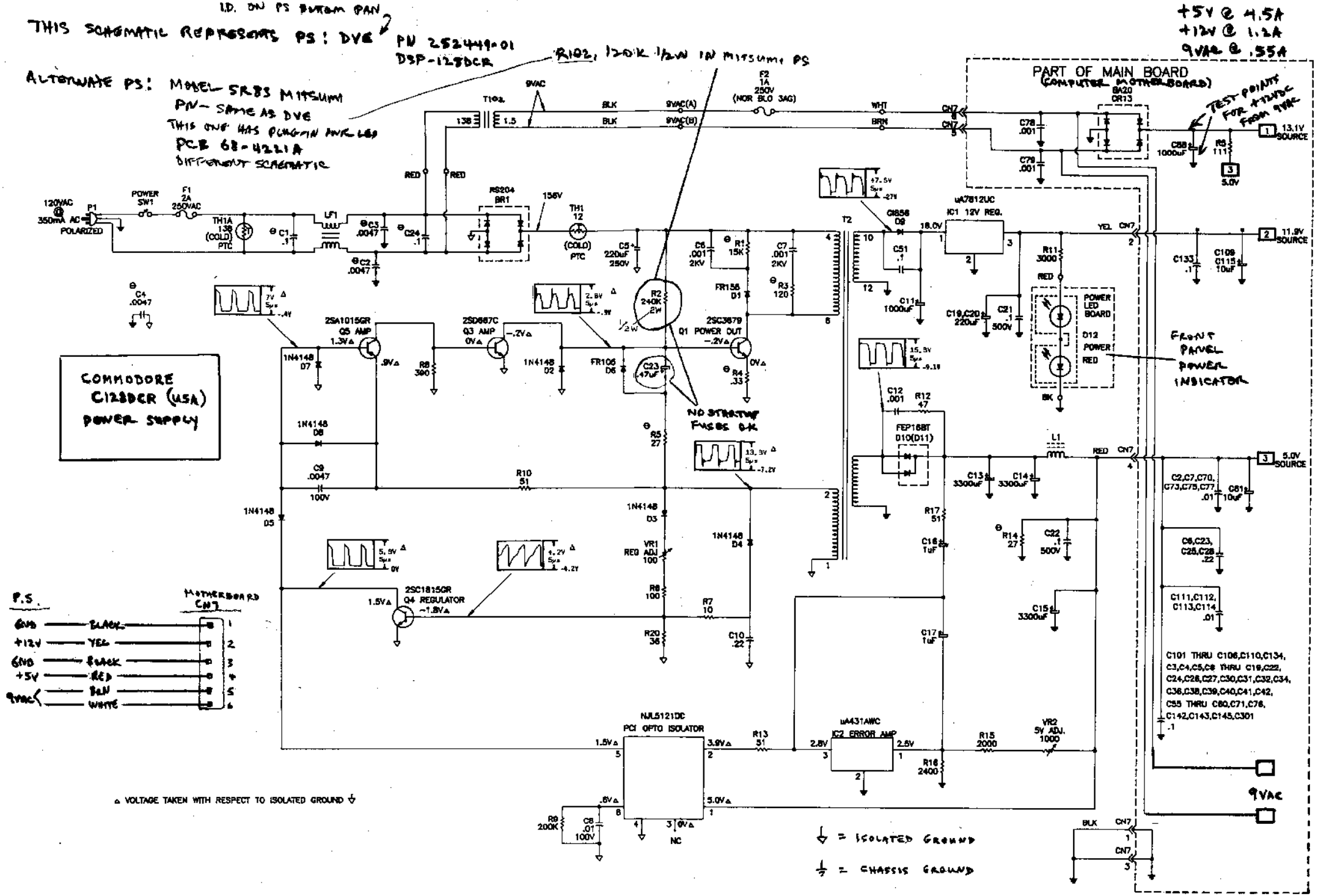
番号	品名	材質	改定	備考
発行	年月日	名称	1403E1	第1版
単位				
尺度				
一般公差		図部	5628-C001-00	
承認				
検図	鎌沢			
製図				
設計			東北金属工業株式会社	

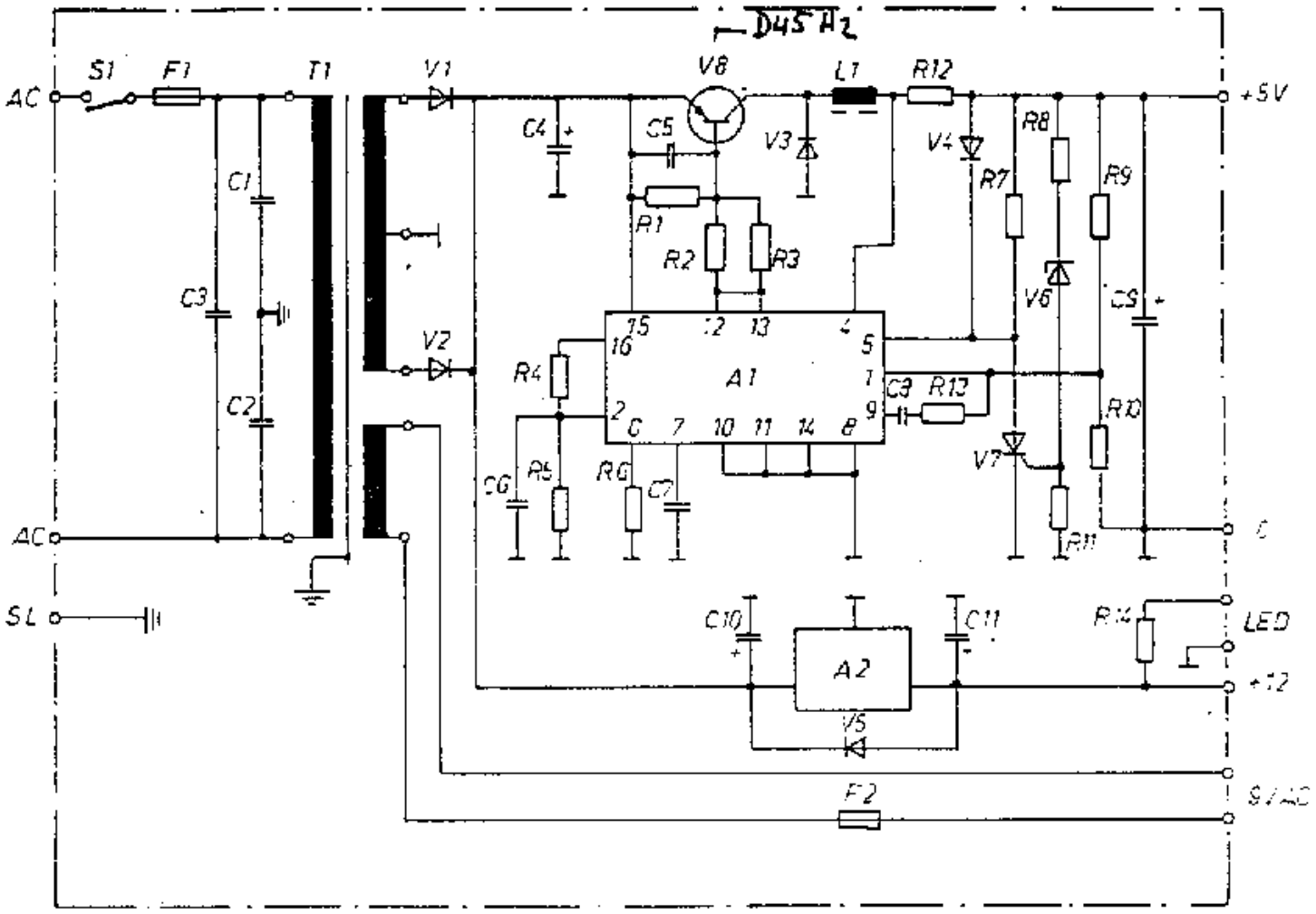


Original-Commodore-Schaltplan für den C128D im Blechgehäuse (C128 DCR). Teil 4: RAM und ROM.



Original-Commodore-Schaltplan für den C128D im Blechgehäuse (C128 DCR). Teil 3: VIC und VDC.


I.D. ON PS BOTTOM PAN
THIS SCHEMATIC REPRESENTS PS: DVE PN 252449-01 DSP-128DCR
R102, 120K 1/2W IN MITSUMI PS
+5V @ 4.5A
+12V @ 1.2A
9VAC @ .55A
ALTERNATE PS: MODEL SR8S MITSUMI
PN- SAME AS DVE
THIS ONE HAS PLUGGIN PWR LED
PCB 68-4221A
DIFFERENT SCHEMATIC
PART OF MAIN BOARD (COMPUTER MOTHERBOARD)
TEST POINTS FOR +12VDC FROM PWR
120VAC @ 350mA AC POLARIZED
POWER SW1
F1 2A 250VAC
F2 1A 250V (NOR BLO 3AG)
9VAC
BLK
WHT
BRN
RED
CN7
13.1V SOURCE
5.0V
11.9V SOURCE
5.0V SOURCE
9VAC
TH1A 13Ω (COLD) PTC
TH1 12 (COLD) PTC
156V
BR1
T1
T2
uA7812UC IC1 12V REG.
18.0V
YEL
2SA1015GR Q5 AMP
2SD667C Q3 AMP
2SC3679 Q1 POWER OUT
FR155 D1
FR106 D6
1N4148 D7
1N4148 D8
1N4148 D2
1N4148 D5
1N4148 D3
1N4148 D4
G856 D9
FEP16BT D10(D11)
POWER LED BOARD
D12 POWER RED
FRONT PANEL POWER INDICATOR
NO STARTUP FUSES O.K.
2SC1815GR Q4 REGULATOR
NJL5121DC PCI OPTO ISOLATOR
uA431AWC IC2 ERROR AMP
VR1 REG ADJ 100
VR2 5V ADJ. 1000
C101 THRU C106,C110,C134, C3,C4,C5,C8 THRU C19,C22, C24,C26,C27,C30,C31,C32,C34, C36,C38,C39,C40,C41,C42, C55 THRU C60,C71,C76, C142,C143,C145,C301
COMMODORE C128DCR (USA) POWER SUPPLY
P.S.
MOTHERBOARD CN7
GND BLACK 1
+12V YEL 2
GND BLACK 3
+5V RED 4
BLK 5
9VAC WHITE 6
△ VOLTAGE TAKEN WITH RESPECT TO ISOLATED GROUND ⏚
⏚ = ISOLATED GROUND
⏚ = CHASSIS GROUND


D45 Hz
S1
F1
T1
V1
V8
L1
R12
+5V
AC
C1
C3
C2
C4
C5
V3
V4
R8
R7
R9
R1
R2
R3
V6
C8
V2
R4
A1
C9
R13
R10
V7
R11
C
C6
R5
R6
C7
SL
C10
A2
C11
R14
LED
+12
V5
F2
S/AC